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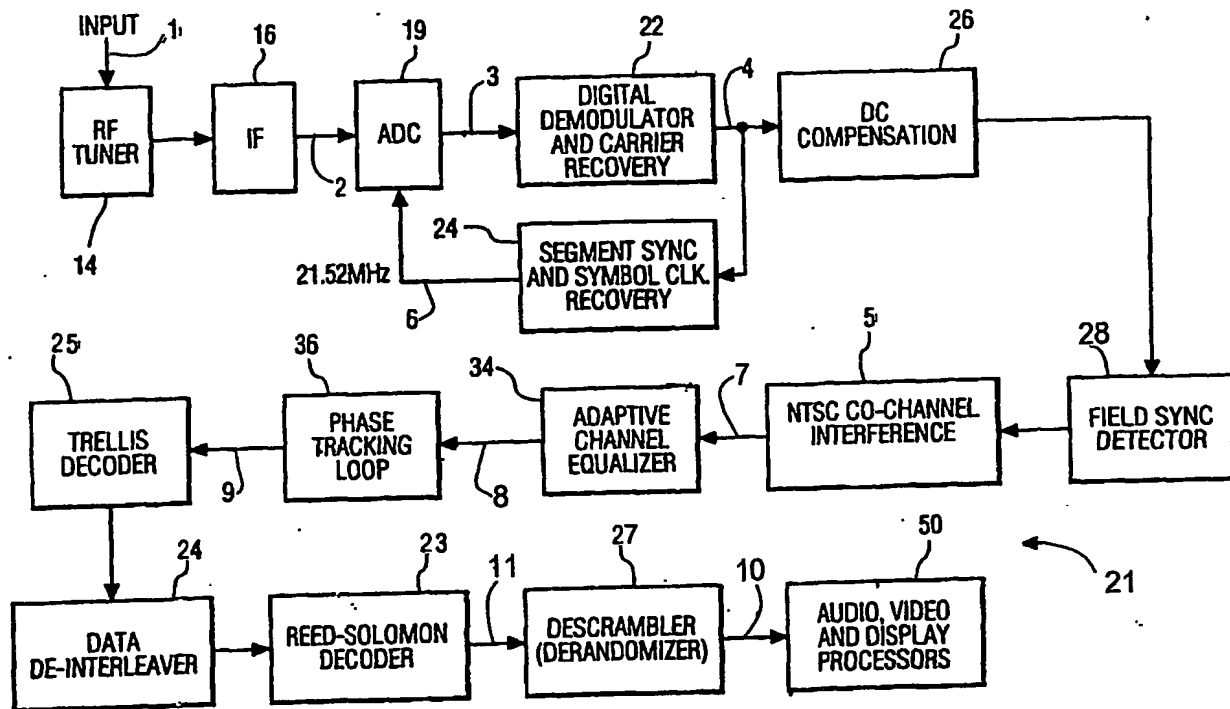


Fig. 1 – Prior art

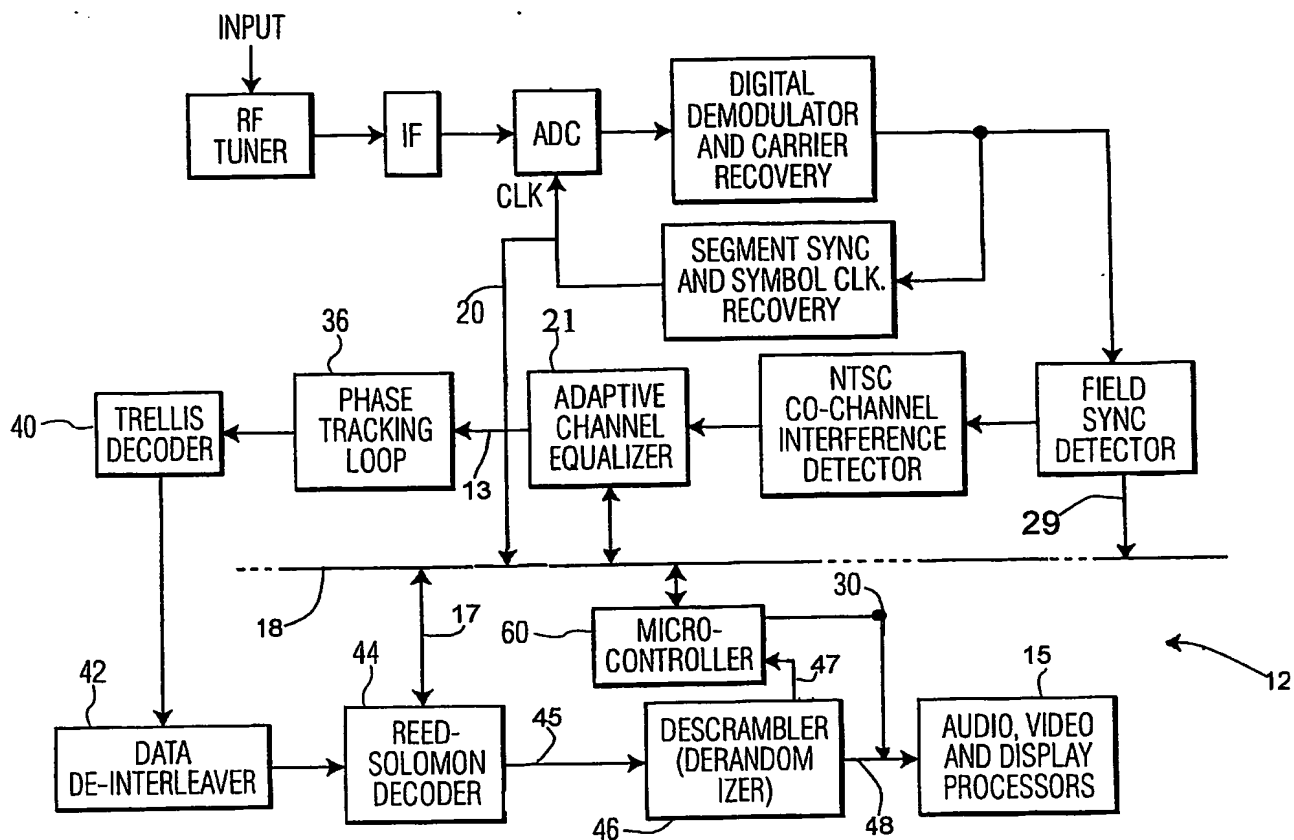


Fig. 2 – System

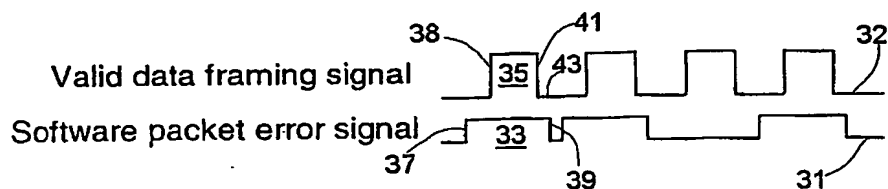


Fig. 3 – Waveforms

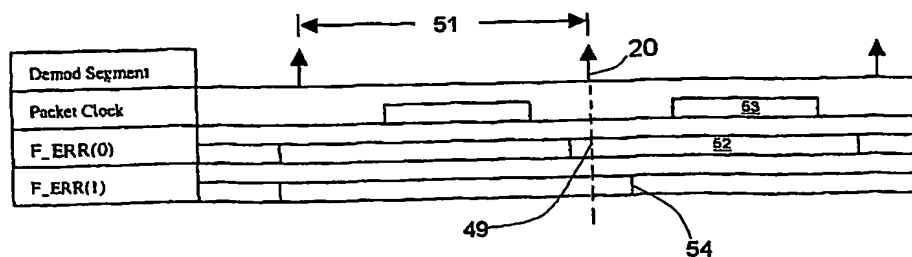


Fig. 4 – Timing

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001	bclr	2, int_stat1	039	ora	#1
002	lda	ms_timer_count	040	bra	apply_gate
003	inca		041	lda	PCNTL
004	sta	ms_timer_count	042	and	#0xFE
005	lda	#0x6D	043	sta	i_temp0
006	sta	CAP_RAM_TRIG	044	lda	sw_ferr_cnt+1
007	lda	#0x2D	045	add	#1
008	sta	CAP_RAM_TRIG	046	sta	sw_ferr_cnt+1
009	lda	CAP_RAM_BASE	047	lda	sw_ferr_cnt
010	and	#0x80	048	adc	#0
011	ldx	ng_ptr_in	049	sta	sw_ferr_cnt
012	sta	next_gate, x	050	lda	i_temp0
013	txa		051	sta	PCNTL
014	eor	#1	052	txa	
015	sta	ng_ptr_in	053	eor	#1
016	inc	pkt_cnt	054	sta	ng_ptr_out
017	lda	pkt_cnt	055	ldx	field_flag
018	cmp	#170	056	beq	no_field
019	bne	enb_wait	057	lda	sync_cnt
020	lda	#1	058	inca	
021	sta	ferr_dly	059	sta	sync_cnt
022	jmp	chk_field	060	cmp	#55
023	lda	ferr_dly	061	bne	no_field
024	nop		062	clr	field_flag
025	deca		063	lda	#15
026	bne	ferr_delay_idle	064	sta	ferr_dly
027	lda	ferr_mod	065	clr	pkt_cnt
028	inca		066	clr	ferr_mod
029	cmp	#3	067	clr	ng_ptr_in
030	bne	store_ferr_mod	068	clr	ng_ptr_out
031	inc	ferr_dly	069	rts	
032	clra		070	bclr	1, int_stat1
033	sta	ferr_mod	071	inc	field_flag
034	ldx	ng_ptr_out	072	lda	ng_ptr_out
035	lda	next_gate, x	073	eor	#1
036	ora	ferr_enable	074	sta	ng_ptr_out
037	bne	gate_on	075	clr	sync_cnt
038	lda	PCNTL	076	rts	

Fig. 5 – Listing

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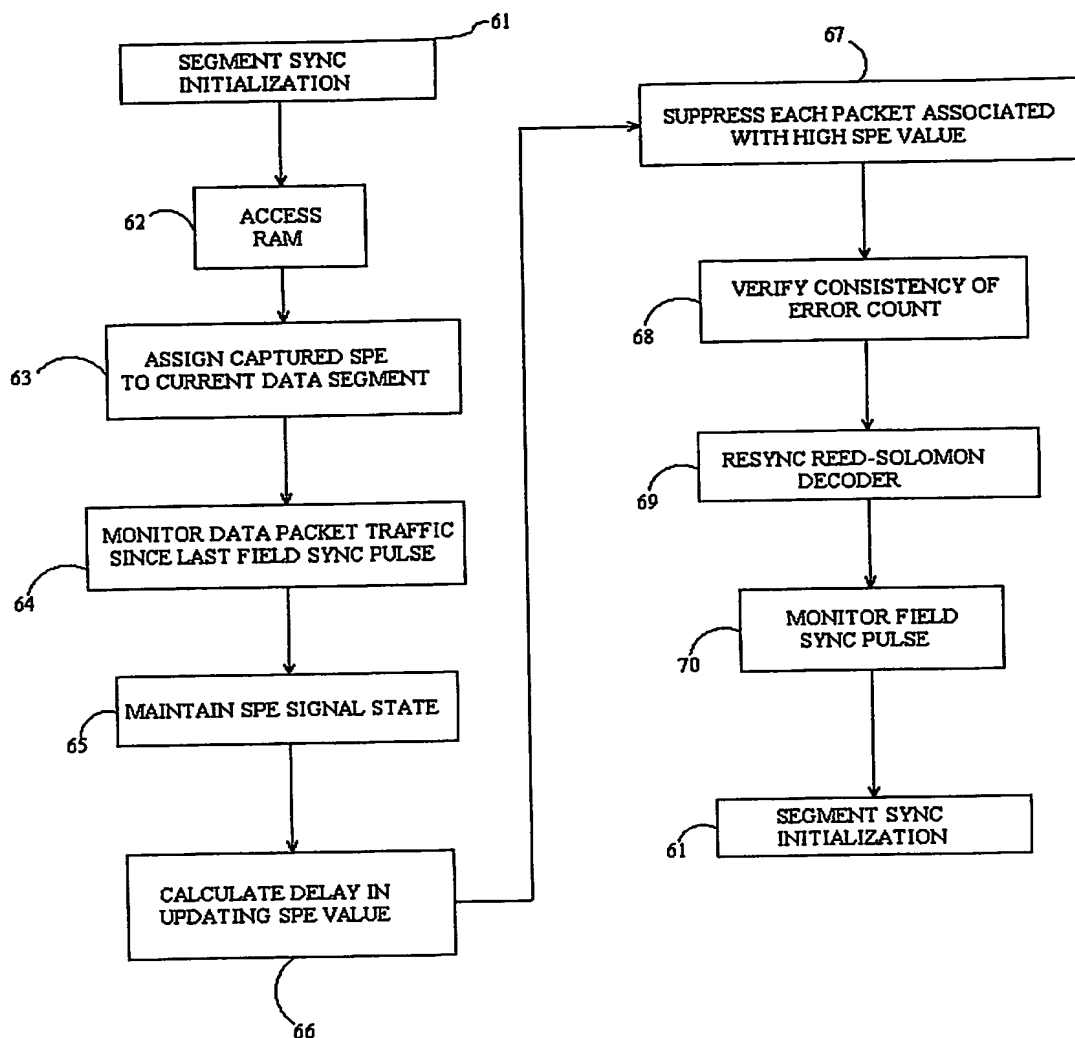


Fig. 6 - Flowchart